

# Canada Research Chair – Tier II

## Sensing and Sensor Technology for Ocean and Underwater Applications

The Department of Electrical and Computer Engineering at Dalhousie University invites applications for a Tier II Canada Research Chair in sensing and sensor technology for ocean and underwater applications. The successful candidate will be appointed to a tenure stream position at the rank of Assistant or Associate Professor with an anticipated start date of July 1, 2017, or as negotiated. Applicants must have a Ph.D. in Electrical and Computer Engineering and be registered Professional Engineers in Canada or be eligible and committed to registration in Nova Scotia.

The ideal candidate will be an outstanding emerging scholar able to develop a research program in ocean and underwater sensor technology. Applicants with a track record of research in a relevant electrical and computer engineering field will be considered, including electronics for sensing, acoustics, sensor networks, electromagnetics, and photonics. The appointee will be expected to establish an independent, high-impact, externally-funded research program, teach and mentor undergraduate and graduate students, and fulfill service mandates.

The Canada Research Chair program was established by the Government of Canada to enable Canadian universities to foster research excellence. Tier II Chairs are for exceptional emerging researchers with potential to be leaders in their fields. More information can be found at [www.chairs-chaieres.gc.ca](http://www.chairs-chaieres.gc.ca). The successful candidate will be required to participate in the preparation of a Canada Research Chair nomination package.

Completed applications, in a single pdf file format, should include:

- a cover letter outlining qualification for the position;
- a curriculum vitae;
- the proposed research program (six-page CRC nomination format preferred);
- a one-page statement describing teaching philosophy and teaching interests;
- three scholarly publications demonstrating contributions to the field; and
- a completed Self-Identification Questionnaire available at [www.dal.ca/becounted/selfid](http://www.dal.ca/becounted/selfid).

Applications should be sent not later than December 15, 2016 to:

Chair of the Search Committee  
c/o Jascinth Butterfield  
Department of Electrical and Computer Engineering  
Dalhousie University  
Halifax, NS Canada B3H 4R2  
Email: [jbutterf@dal.ca](mailto:jbutterf@dal.ca)

Dalhousie University is recognized internationally for our world-class academic programs and as one of Canada's leading research institutions. With our 200<sup>th</sup> anniversary on the horizon in 2018, Dalhousie welcomes talented scholars to our home by the ocean and to join our mission to make a lasting impact through the discovery, advancement and sharing of knowledge.

Dalhousie is also home to the headquarters of the Ocean Frontier Institute (OFI; [www.dal.ca/ofi](http://www.dal.ca/ofi)). As an international hub for ocean science focused on the Northwest Atlantic and Canada's Arctic gateway, OFI will bring together elite researchers and institutes from across the globe to understand our changing oceans and create safe, sustainable solutions for ocean development. Including a \$93.7M award through the Canada First Research Excellence Fund program (CFREF; [www.cfref-apogee.gc.ca](http://www.cfref-apogee.gc.ca)), government, private and partner contributions, the OFI is a \$220M enterprise.

This Tier II CRC is reserved for external recruitment. Only candidates who are external to Dalhousie University may apply. Dalhousie is committed to fostering a collegial culture grounded in diversity and inclusiveness. The university encourages applications from qualified Aboriginal people, persons with a disability, racially visible persons, women, persons of minority sexual orientations and gender identities, and all qualified candidates who would contribute to the diversity of our community. All qualified candidates are encouraged to apply; however, Canadians and permanent residents will be given priority.